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Title of Invention	BRANCH INSTRUCTION FOR PROCESSOR ARCHITECTURE
Application Number	: 10/069229
Date :	2002-02-22
First Named Applica	nt: Wolrich Gilbert
Confirmation Number	n: 1429
Attorney Docket Nu	nber: 10559-305US1

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Documents being submitted:	Files	
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Comments		
IDS # 3 of 4 for U.S. Application Serial No. 1	0/069,229	